TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

TA8255AH

MAX POWER 22W BTL×4CH AUDIO POWER IC

The TA8255AH is 4ch BTL audio power amplifier for consumer application.

It is designed low distortion ratio for 4ch BTL audio power amplifier, built-in Stand-by Function, Muting Function and Junction Temperature Detection Circuit. Additionally, the AUX. amplifier is built-in, it can make the beep signal etc. output to 2 channels (OUT1 and 4). It contains various kind of protectors for car audio.

FEATURES

- High power
 - : $P_{OUT}(MAX) = 22W (Typ.)$

 $(V_{CC} = 13.7V, f = 1kHz, R_1 = 4\Omega)$

: POUT(1) = 17W(Typ.)

 $(V_{CC} = 14.4V, f = 1kHz, THD = 10\%, R_L = 4\Omega)$

: $P_{OUT}(2) = 14W (Typ.)$

 $(V_{CC} = 13.2V, f = 1kHz, THD = 10\%, R_L = 4\Omega)$

- Low distortion ratio
 - : THD = 0.02% (Typ.)

$$(V_{CC} = 13.2V, f = 1kHz, P_{OUT} = 3W, R_{L} = 4\Omega)$$

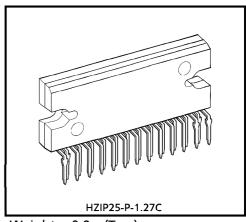
- Low noise
 - : $V_{NO} = 0.10 \text{mV}_{rms}$ (Typ.)

$$(V_{CC} = 13.2V, R_{Q} = 0\Omega, G_{V} = 34dB, BW = 20\sim20kHz)$$

- Built-in stand-by switch function (Pin2)
- Built-in muting function (Pin①, ②)
- Built-in AUX. amplifier from single input to 2 channels output (Pin[®])
- Built-in junction temperature detection circuit (Pin24)
 - : Pin DC voltage rises at about +10mV/°C in proportion to junction temperature.

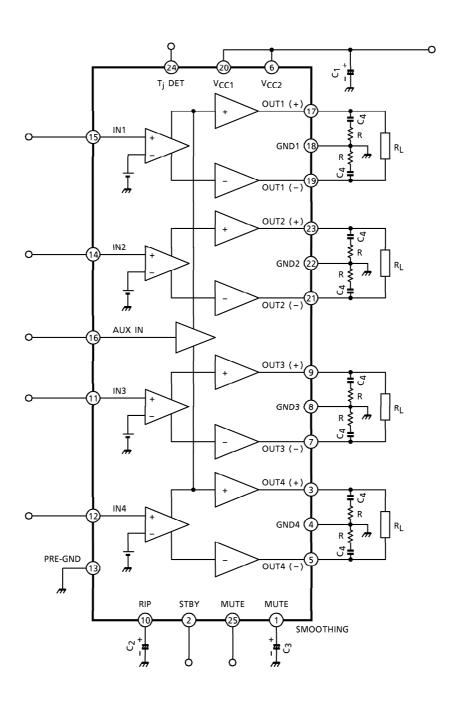
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- Built-in various protection circuit
 - : Thermal shut down, over voltage, out to GND, out to V_{CC}, out to out short
- Operating supply voltage
 - : $V_{CC (opr)} = 9 \sim 18V$



Weight: 9.8g (Typ.)

BLOCK DIAGRAM

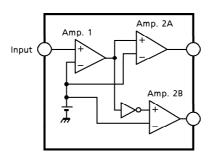


CAUTION AND APPLICATION METHOD

(Description is made only on the single channel.)

1. Voltage gain adjustment

This IC has no NF (negative feedback) terminals. Therefore, the voltage gain can't adjusted, but it makes the device a space and total costs saver.



(Fig.1) Block diagram

The voltage gain of Amp. 1 : $G_{V1} = 0dB$ The voltage gain of Amp. 2A, B : $G_{V2} = 28dB$ The voltage gain of BLT Connection : $G_{V(BTL)} = 6dB$

Therefore, the total voltage gain is decided by expression below.

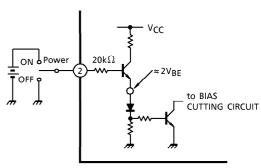
$$G_V = G_{V1} + G_{V2} + G_{V(BTL)} = 0 + 28 + 6 = 34dB$$

2. Stand-by SW function

By means of controlling pin (Stand-by terminal) to High and Low, the power supply can be set to ON and OFF. The threshold voltage of pin is set at about 3V (Typ.), and the Power Supply current is about 100μ A (Typ.) at the stand-by state.

Control Voltage of pin②: V (SB)

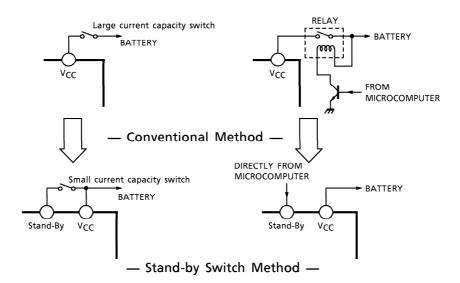
| STAND-BY | POWER | V (SB) (V) |
|----------|-------|-------------------|
| ON | OFF | 0~2 |
| OFF | ON | 3~V _{CC} |



(Fig.2) With pin② set to High, Power is turned ON

Adjustage of Stand-by SW

- (1) Since V_{CC} can directly be controlled to ON or OFF by the microcomputer, the switching relay can be omitted.
- (2) Since the control current is microscopic, the switching relay of small current capacity is satisfactory for switching



3. Preventive measure against oscillation

For preventing the oscillation, it is advisable to use C₄, the condenser of polyester film having small characteristic fluctuation of the temperature and the frequency.

The resistance R to be series applied to C₄ is effective for phase correction of high frequency, and improves the oscillation allowance.

- (1) Capacity value and the kind of condenser
- (2) Layout of printed board

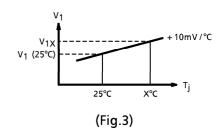
4. Junction temperature detecting pin@

Using temperature characteristic of a band gap circuit and in proportion to junction temperature, pin 4 DC voltage: V_2 rises at about $+10\text{mV}/^{\circ}\text{C}$ temperature characteristic. So, the relation between V_2 at $T_j = 25^{\circ}\text{C}$ and V_{2x} at $T_j = x^{\circ}\text{C}$ is decided by the following expression:

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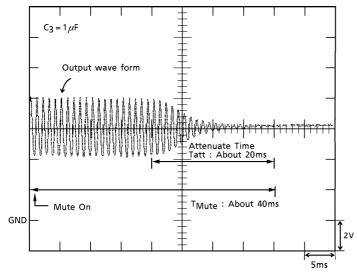
T (x°C) =
$$\frac{V_{2x} - V_2 (25^{\circ}C)}{10 \text{mV} / {^{\circ}C}} + 25 (^{\circ}C)$$

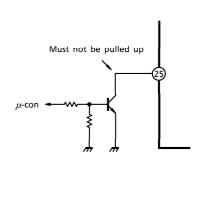
In deciding a heat sink size, a junction temperature can be easily made clear by measuring voltage at this pin while a backside temperature of IC was so far measured using a thermocouple type thermometer.



5. Muting function: pin①, pin®

By means of controlling pin® (Mute control terminal) less than about 1.5V, it can make the IC muting condition as below. However, pin® must not be connected to a certain voltage, for example, V_{CC}, V_{DD}, V_{ref},····etc. In other words, pin® is inhibited to be pulled up, for instance fig.5 application.





(Fig.4) Output wave form at Muting Condition

(Fig.5) Mute control

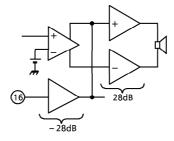
The attenuation by the muting function is 70dB (Typ.). This muting is very smooth attenuating by the time constant of pin①: smoothing.

Therefore, this function is suitable to the audio muting. The time for attenuation: Tatt is adjustable by changing the capacitance of C_3 . But the Tatt may influence the popping noise level. So, please decide the time of Tatt by testing on the units.

6. AUX. amplifier: pin 16

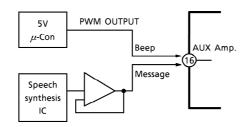
The $pin \ensuremath{\mathfrak{B}}$ is for input terminal of AUX. amplifier.

The total gain is 0dB by using of AUX. amplifier.



(Fig.6) AUX. amplifier

Therefore, the $\mu\text{-Con}$ can directly drive the AUX. amplifier.



(Fig.7) The application of AUX. amplifier

The amplified signal from pin® is out to the OUT1 and 4.

7. Cross talk

The cross talk characteristics of the IC is not good between OUT1 and 2, OUT3 and 4. So we recommend to use by below method.

| OUT1, 2 | L-ch (or R-ch) |
|---------|----------------|
| OUT3, 4 | R-ch (or L-ch) |

And, please refer to below table in case of applying the AUX. IN because it is out to OUT 1 and 4.

ex)

| OUT1 | Front | L-ch (or R-ch) | AUX. OUT | | |
|------|-------|------------------|----------|--|--|
| OUT2 | Rear | L-CII (OI K-CII) | | | |
| OUT3 | Rear | Pich (or Lich) | _ | | |
| OUT4 | Front | R-ch (or L-ch) | AUX. OUT | | |

MAXIMUM RATING (Ta = 25°C)

| CHARACTERISTIC | SYMBOL | RATING | UNIT |
|----------------------------|-------------------------|-----------------|------|
| Peak Supply Voltage (0.2s) | V _{CC} (surge) | 50 | V |
| DC Supply Voltage | V _{CC} (DC) | 25 | V |
| Operating Supply Voltage | V _{CC (opr)} | 18 | V |
| Output Current (peak) | l _{o (peak)} | 9 | Α |
| Power Dissipation | P _D (*) | 83 | W |
| Operating Temperature | T _{opr} | - 40~85 | °C |
| Storage Temperature | T _{stg} | - 55∼150 | °C |

(*) Package thermal resistance $\theta_{j-T} = 1.5^{\circ}\text{C/W}$ (Typ.) (Ta = 25°C, with infinite heat sink)

ELECTRICAL CHARACTERISTICS (Unless otherwise specified $V_{CC} = 13.2V$, f = 1kHz, $R_L = 4\Omega$, $Ta = 25^{\circ}C$)

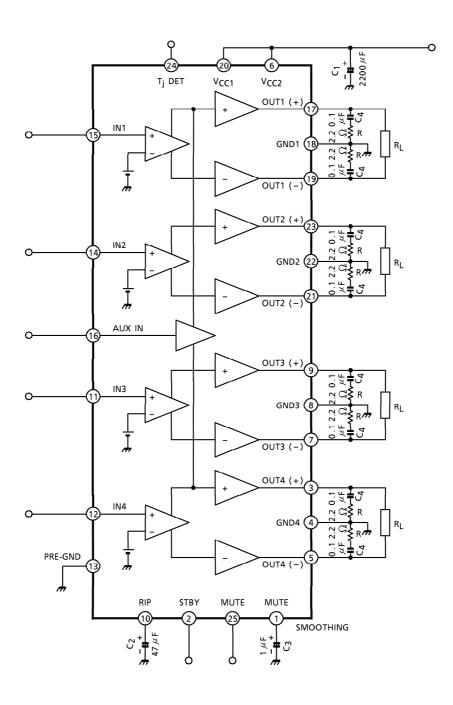
| CHARACTERISTIC | SYMBOL | TEST CIR- CUIT | TEST CONDITION | MIN. | TYP. | MAX. | UNIT |
|------------------------------|------------------------|----------------------|---|-------|------|-------|------------|
| Quiescent Current | lccQ | _ | V _{IN} = 0 | _ | 200 | 400 | mA |
| Output Power | P _{OUT} (MAX) | _ | V _{CC} = 13.7V, MAX power | _ | 22 | _ | |
| | Pout (1) | _ | V _{CC} = 14.4V, THD = 10% | _ | 17 | _ | w |
| | POUT (2) | _ | THD = 10% | 10 | 14 | _ | |
| Total Harmonic Distortion | THD | _ | P _{OUT} = 3W | _ | 0.02 | 0.2 | % |
| Voltage Gain | GV | _ | $V_{OUT} = 0.775V_{rms}$ (0dBm) | 32 | 34 | 36 | dB |
| Voltage Gain Ratio | ⊿G _V | _ | $V_{OUT} = 0.775V_{rms}$ (0dBm) | - 1.0 | 0 | 1.0 | dB |
| Output Noise Voltage | V _{NO} (1) | _ | $R_g = 0\Omega$, DIN45405 | _ | 0.12 | _ | mV_{rms} |
| | V _{NO} (2) | _ | $R_g = 0\Omega$, BW = 20Hz~20kHz | _ | 0.10 | 0.35 | mV_{rms} |
| Ripple Rejection Ratio | R.R. | _ | f_{rip} = 100Hz, R_g = 620 Ω V_{rip} = 0.775 V_{rms} (0dBm) | 40 | 55 | _ | dB |
| Cross Talk | C.T. | _ | $R_g = 620\Omega$, $V_{OUT} = 0.775V_{rms}$ (0dBm) | _ | 75 | _ | dB |
| Output Offset Voltage | VOFFSET | _ | _ | - 300 | 0 | + 300 | mV |
| Input Resistance | RIN | _ | _ | _ | 30 | _ | kΩ |
| Stand-by Current | ISB | _ | Stand-by condition | _ | 100 | 150 | μΑ |
| Stand-by Control | V _{SB} H | _ | Power : on | 3.0 | _ | VCC | V |
| Voltage | V _{SB} L | _ | Power : off | 0 | _ | 1.5 | |
| Mute Control Voltage | V _M H | _ | Mute : off | | OPEN | | V |
| (*) | V _M L | _ | Mute : on | 0 | _ | 1.5 | |
| Mute Attenuation | ATT M | _ | Mute : on | _ | 70 | _ | dB |

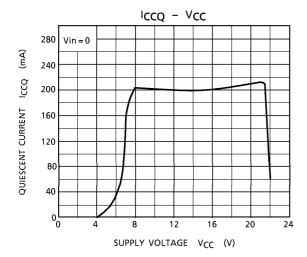
(*) Muting function must be controlled by open and Low Logic.

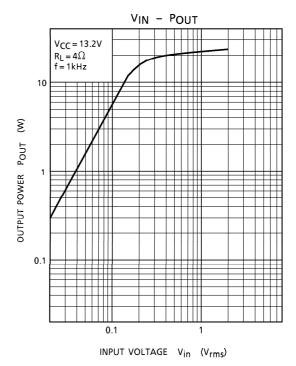
This means that the mute control terminal: pin must not be pulled up.

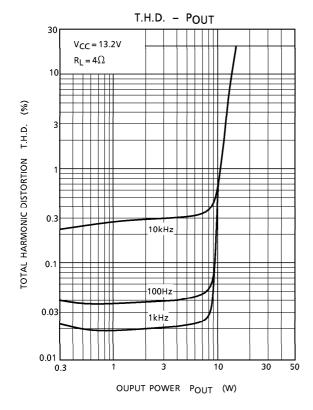
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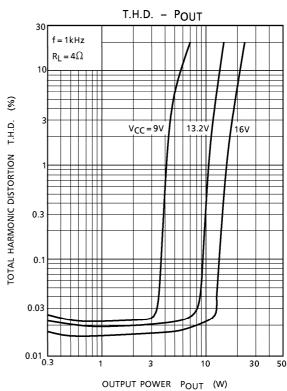
TEST CIRCUIT $(G_V = 34dB)$

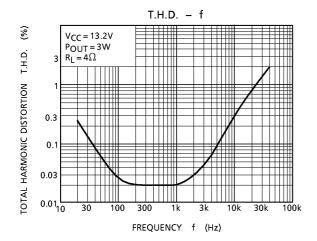


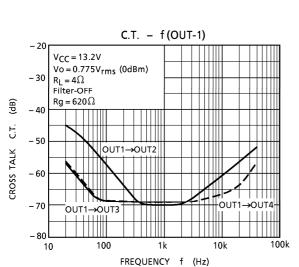


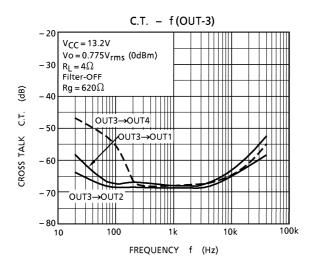


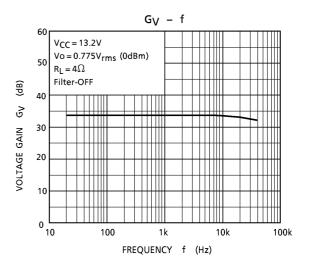


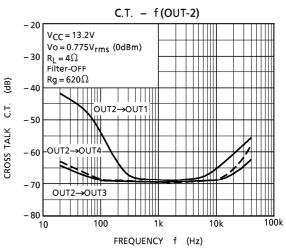


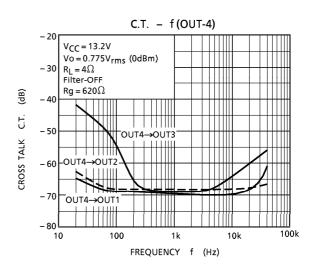


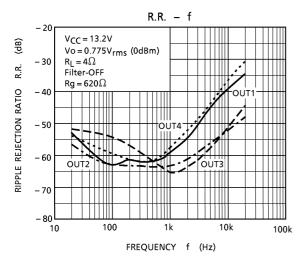


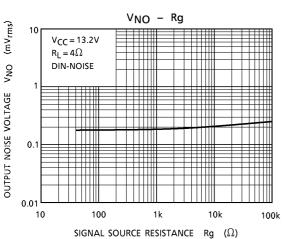


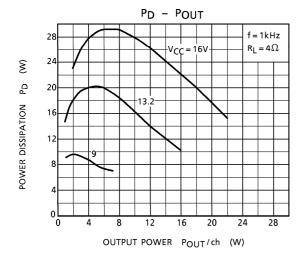


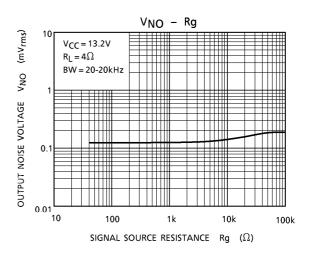


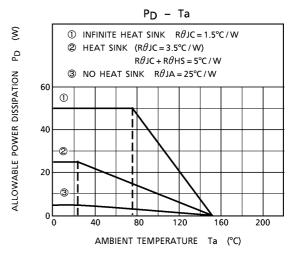




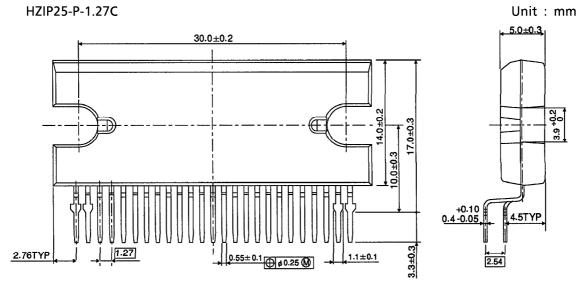


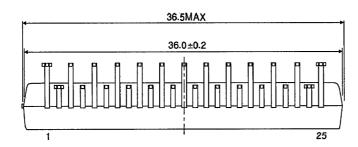






PACKAGE DIMENSIONS





Weight: 9.8g (Typ.)

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