

High-Speed, Single-Supply, Rail-to-Rail OPERATIONAL AMPLIFIERS *MicroAmplifier™* Series

FEATURES

- RAIL-TO-RAIL INPUT
- RAIL-TO-RAIL OUTPUT (within 10mV)
- WIDE BANDWIDTH: 38MHz
- HIGH SLEW RATE: 22V/ μ s
- LOW NOISE: 5nV/ $\sqrt{\text{Hz}}$
- LOW THD+NOISE: 0.0006%
- UNITY-GAIN STABLE
- *Micro*SIZE PACKAGES
- SINGLE, DUAL, AND QUAD

APPLICATIONS

- CELL PHONE PA CONTROL LOOPS
- DRIVING A/D CONVERTERS
- VIDEO PROCESSING
- DATA ACQUISITION
- PROCESS CONTROL
- AUDIO PROCESSING
- COMMUNICATIONS
- ACTIVE FILTERS
- TEST EQUIPMENT

DESCRIPTION

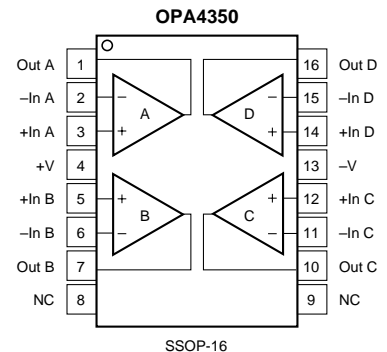
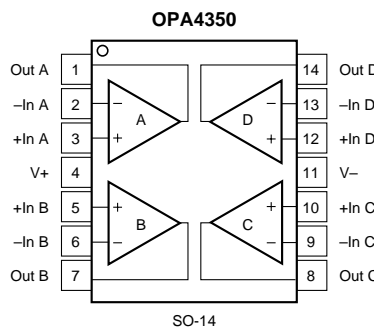
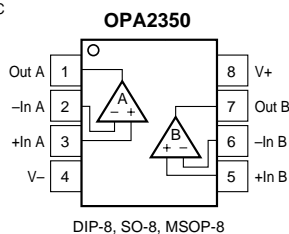
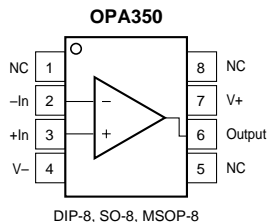
OPA350 series rail-to-rail CMOS operational amplifiers are optimized for low voltage, single-supply operation. Rail-to-rail input/output, low noise (5nV/ $\sqrt{\text{Hz}}$), and high speed operation (38MHz, 22V/ μ s) make them ideal for driving sampling Analog-to-Digital (A/D) converters. They are also well suited for cell phone PA control loops and video processing (75 Ω drive capability) as well as audio and general purpose applications. Single, dual, and quad versions have identical specifications for maximum design flexibility.

The OPA350 series operates on a single supply as low as 2.5V with an input common-mode voltage range that extends

300mV below ground and 300mV above the positive supply. Output voltage swing is to within 10mV of the supply rails with a 10k Ω load. Dual and quad designs feature completely independent circuitry for lowest crosstalk and freedom from interaction.

The single (OPA350) and dual (OPA2350) come in the miniature MSOP-8 surface mount, SO-8 surface mount, and DIP-8 packages. The quad (OPA4350) packages are the space-saving SSOP-16 surface mount and SO-14 surface mount. All are specified from -40°C to +85°C and operate from -55°C to +150°C.

SPICE Model available at www.ti.com



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

ABSOLUTE MAXIMUM RATINGS⁽¹⁾

Supply Voltage	5.5V
Signal Input Terminals, Voltage ⁽²⁾	(V-) – 0.3V to (V+) + 0.3V
Current ⁽²⁾	10mA
Output Short Circuit ⁽³⁾	Continuous
Operating Temperature	–55°C to +150°C
Storage Temperature	–55°C to +150°C
Junction Temperature	150°C
Lead Temperature (soldering, 10s)	300°C

NOTES: (1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. (2) Input terminals are diode-clamped to the power supply rails. Input signals that can swing more than 0.3V beyond the supply rails should be current-limited to 10mA or less. (3) Short circuit to ground, one amplifier per package.



ELECTROSTATIC DISCHARGE SENSITIVITY

This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

PACKAGE/ORDERING INFORMATION

PRODUCT	PACKAGE	PACKAGE DRAWING NUMBER	PACKAGE DESIGNATOR	SPECIFIED TEMPERATURE RANGE	PACKAGE MARKING	ORDERING NUMBER ⁽¹⁾	TRANSPORT MEDIA
Single							
OPA350EA	MSOP-8	337	DGK	–40°C to +85°C	C50	OPA350EA/250	Tape and Reel
"	"	"	"	"	"	OPA350EA/2K5	Tape and Reel
OPA350UA	SO-8	182	D	–40°C to +85°C	OPA350UA	OPA350UA	Rails
"	"	"	"	"	"	OPA350UA/2K5	Tape and Reel
OPA350PA	DIP-8	006	P	–40°C to +85°C	OPA350PA	OPA350PA	Rails
Dual							
OPA2350EA	MSOP-8	337	DGK	–40°C to +85°C	D50	OPA2350EA/250	Tape and Reel
"	"	"	"	"	"	OPA2350EA/2K5	Tape and Reel
OPA2350UA	SO-8	182	D	–40°C to +85°C	OPA2350UA	OPA2350UA	Rails
"	"	"	"	"	"	OPA2350UA/2K5	Tape and Reel
OPA2350PA	DIP-8	006	P	–40°C to +85°C	OPA2350PA	OPA2350PA	Rails
Quad							
OPA4350EA	SSOP-16	322	DBQ	–40°C to +85°C	OPA4350EA	OPA4350EA/250	Tape and Reel
"	"	"	"	"	"	OPA4350EA/2K5	Tape and Reel
OPA4350UA	SO-14	235	D	–40°C to +85°C	OPA4350UA	OPA4350UA	Rails
"	"	"	"	"	"	OPA4350UA/2K5	Tape and Reel

NOTES: (1) Models with a slash (/) are available only in Tape and Reel in the quantities indicated (e.g., /2K5 indicates 2500 devices per reel). Ordering 2500 pieces of "OPA2350EA/2K5" will get a single 2500-piece Tape and Reel.

ELECTRICAL CHARACTERISTICS: $V_S = 2.7V$ to $5.5V$

Boldface limits apply over the specified temperature range, $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$. $V_S = 5V$.

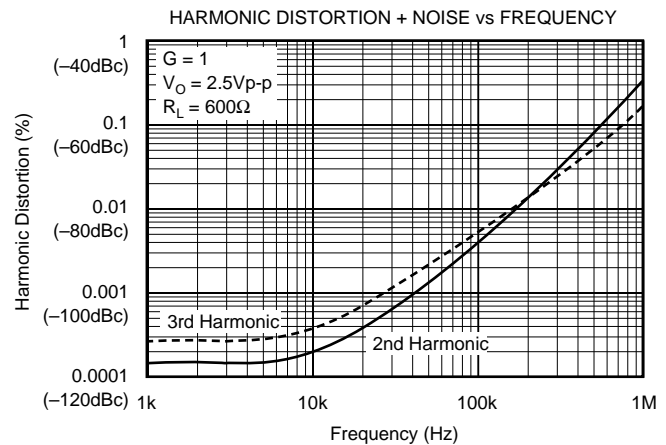
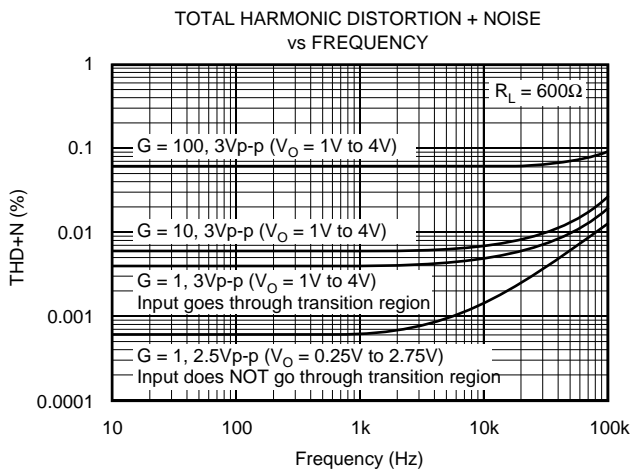
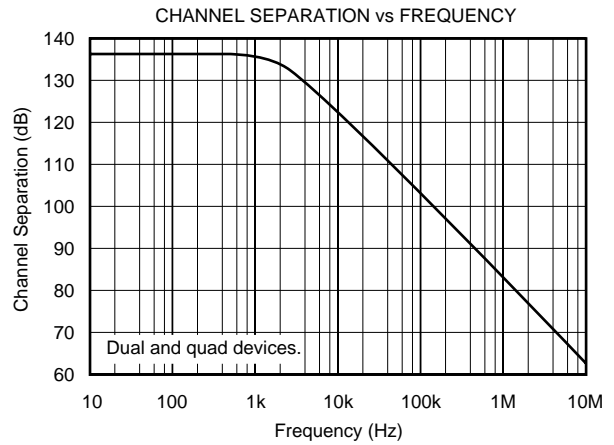
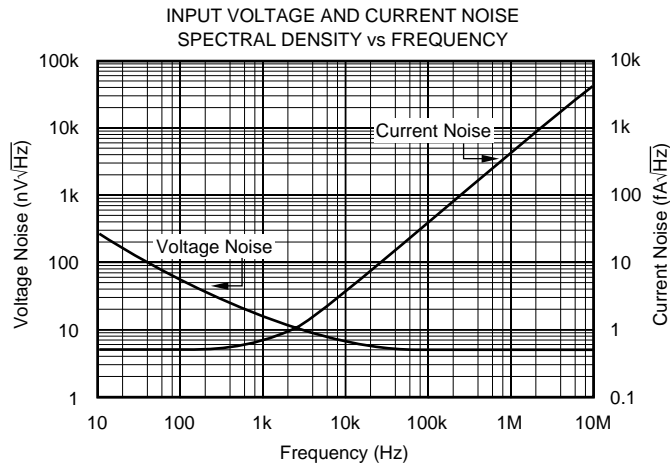
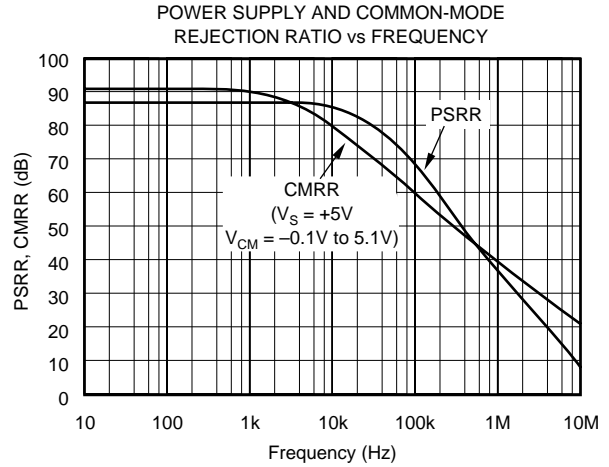
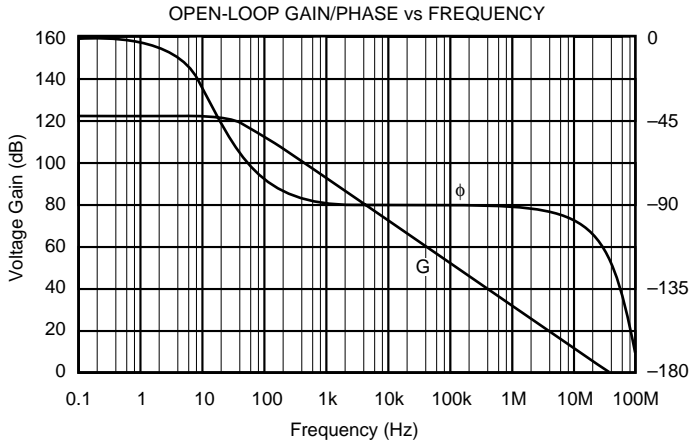
At $T_A = +25^\circ\text{C}$, $R_L = 1\text{k}\Omega$ connected to $V_S/2$ and $V_{OUT} = V_S/2$, unless otherwise noted.

PARAMETER	CONDITION	OPA350EA, UA, PA OPA2350EA, UA, PA OPA4350EA, UA			UNITS
		MIN	TYP ⁽¹⁾	MAX	
OFFSET VOLTAGE Input Offset Voltage V_{OS} $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$ vs Temperature vs Power-Supply Rejection Ratio PSRR $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$ Channel Separation (dual, quad)	$V_S = 5V$ $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$ $V_S = 2.7V$ to $5.5V$, $V_{CM} = 0V$ $V_S = 2.7V$ to $5.5V$, $V_{CM} = 0V$ dc		± 150 ± 4 40 0.15	± 500 ± 1 150 175	μV mV $\mu\text{V}/^\circ\text{C}$ $\mu\text{V}/\text{V}$ $\mu\text{V}/\text{V}$ $\mu\text{V}/\text{V}$
INPUT BIAS CURRENT Input Bias Current I_B vs Temperature Input Offset Current I_{OS}			See Typical Characteristics ± 0.5 ± 0.5	± 10 ± 10	pA pA
NOISE Input Voltage Noise, $f = 100\text{Hz}$ to 400kHz Input Voltage Noise Density, $f = 10\text{kHz}$ e_n $f = 100\text{kHz}$ Current Noise Density, $f = 10\text{kHz}$ i_n			4 7 5 4		μVrms $\text{nV}/\sqrt{\text{Hz}}$ $\text{nV}/\sqrt{\text{Hz}}$ $\text{fA}/\sqrt{\text{Hz}}$
INPUT VOLTAGE RANGE Common-Mode Voltage Range V_{CM} Common-Mode Rejection Ratio CMRR $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$	$T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$ $V_S = 2.7V$, $-0.1V < V_{CM} < 2.8V$ $V_S = 5.5V$, $-0.1V < V_{CM} < 5.6V$ $V_S = 5.5V$, $-0.1V < V_{CM} < 5.6V$	-0.1 66 76 74		(V+)+0.1 84 90	V dB dB dB
INPUT IMPEDANCE Differential Common-Mode			$10^{13} \parallel 2.5$ $10^{13} \parallel 6.5$		$\Omega \parallel \text{pF}$ $\Omega \parallel \text{pF}$
OPEN-LOOP GAIN Open-Loop Voltage Gain A_{OL} $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$	$R_L = 10\text{k}\Omega$, $50\text{mV} < V_O < (V+) - 50\text{mV}$ $R_L = 10\text{k}\Omega$, $50\text{mV} < V_O < (V+) - 50\text{mV}$ $R_L = 1\text{k}\Omega$, $200\text{mV} < V_O < (V+) - 200\text{mV}$ $R_L = 1\text{k}\Omega$, $200\text{mV} < V_O < (V+) - 200\text{mV}$	100 100 100 100	122 120		dB dB dB dB
FREQUENCY RESPONSE Gain-Bandwidth Product GBW Slew Rate SR Settling Time, 0.1% 0.01% Overload Recovery Time Total Harmonic Distortion + Noise THD+N Differential Gain Error Differential Phase Error	$C_L = 100\text{pF}$ $G = 1$ $G = 1$ $G = \pm 1$, 2V Step $G = \pm 1$, 2V Step $V_{IN} \cdot G = V_S$ $R_L = 600\Omega$, $V_O = 2.5V_p$ -p ⁽²⁾ , $G = 1$, $f = 1\text{kHz}$ $G = 2$, $R_L = 600\Omega$, $V_O = 1.4V$ ⁽³⁾ $G = 2$, $R_L = 600\Omega$, $V_O = 1.4V$ ⁽³⁾		38 22 0.22 0.5 0.1 0.0006 0.17 0.17		MHz V/ μs μs μs μs % % deg
OUTPUT Voltage Output Swing from Rail ⁽⁴⁾ V_{OUT} $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$ Output Current I_{OUT} Short-Circuit Current I_{SC} Capacitive Load Drive C_{LOAD}	$R_L = 10\text{k}\Omega$, $A_{OL} \geq 100\text{dB}$ $R_L = 10\text{k}\Omega$, $A_{OL} \geq 100\text{dB}$ $R_L = 1\text{k}\Omega$, $A_{OL} \geq 100\text{dB}$ $R_L = 1\text{k}\Omega$, $A_{OL} \geq 100\text{dB}$		10 25 ± 40 ⁽⁵⁾ ± 80	50 200 200	mV mV mV mA mA
POWER SUPPLY Operating Voltage Range V_S Minimum Operating Voltage Quiescent Current (per amplifier) I_Q $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$	$T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$ $I_Q = 0$ $I_Q = 0$	2.7		5.5 2.5 7.5 8.5	V V mA mA
TEMPERATURE RANGE Specified Range Operating Range Storage Range Thermal Resistance θ_{JA} MSOP-8 Surface Mount SO-8 Surface Mount DIP-8 SO-14 Surface Mount SSOP-16 Surface Mount		-40 -55 -55		+85 +150 +150	$^\circ\text{C}$ $^\circ\text{C}$ $^\circ\text{C}$ $^\circ\text{C}/\text{W}$ $^\circ\text{C}/\text{W}$ $^\circ\text{C}/\text{W}$ $^\circ\text{C}/\text{W}$ $^\circ\text{C}/\text{W}$

NOTES: (1) $V_S = +5V$. (2) $V_{OUT} = 0.25V$ to $2.75V$. (3) NTSC signal generator used. See Figure 6 for test circuit. (4) Output voltage swings are measured between the output and power supply rails. (5) See typical characteristic, "Output Voltage Swing vs Output Current."

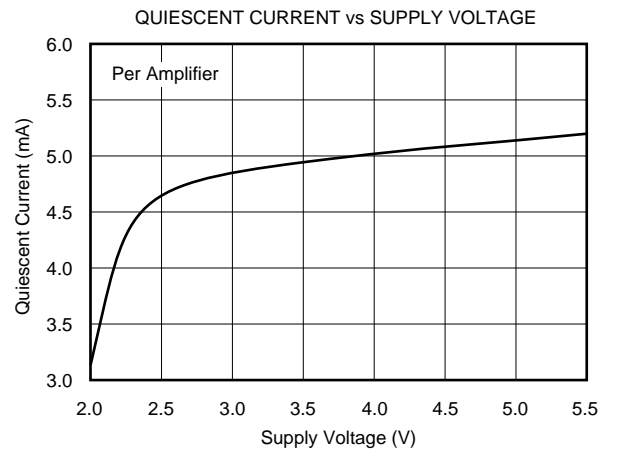
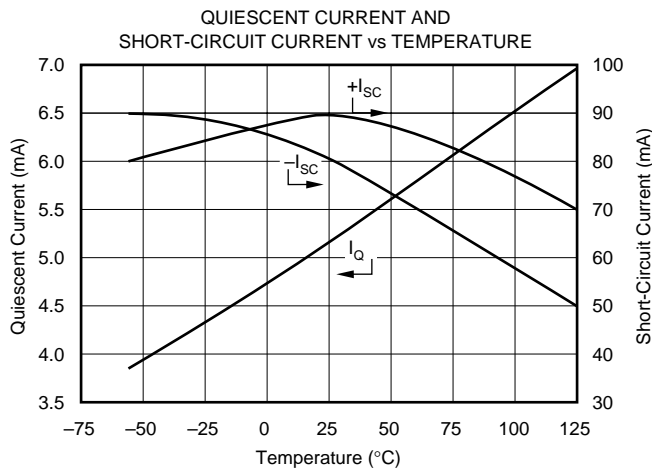
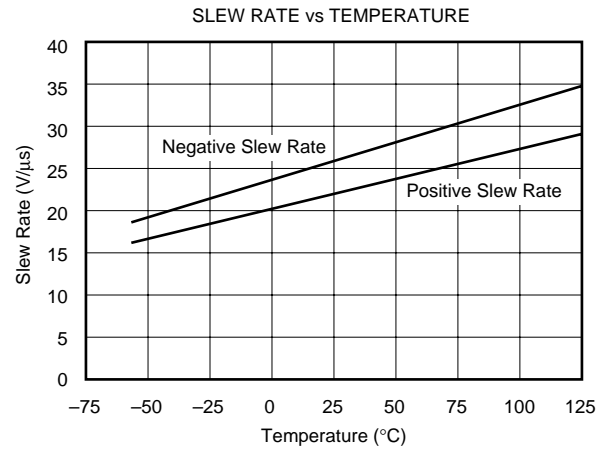
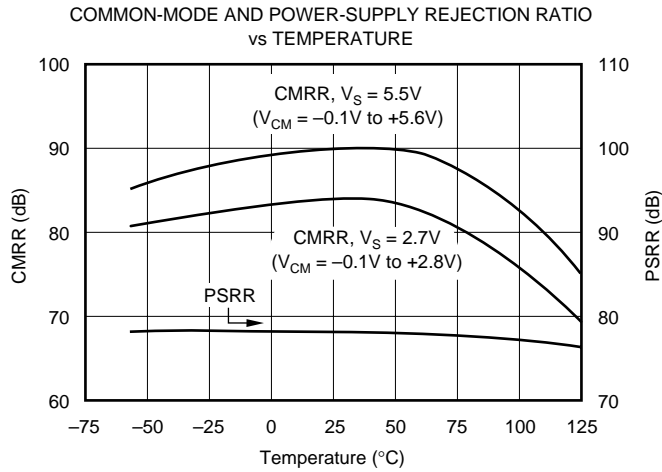
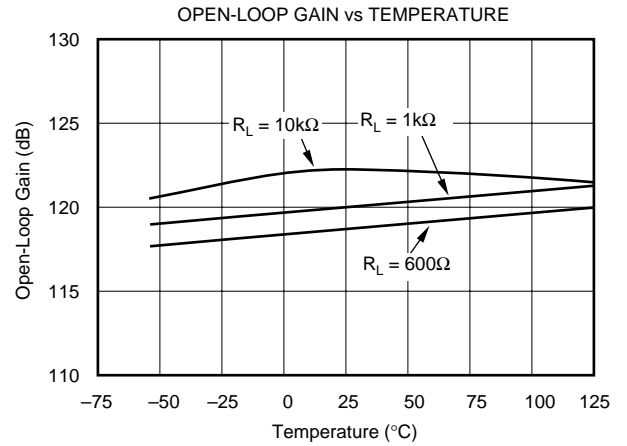
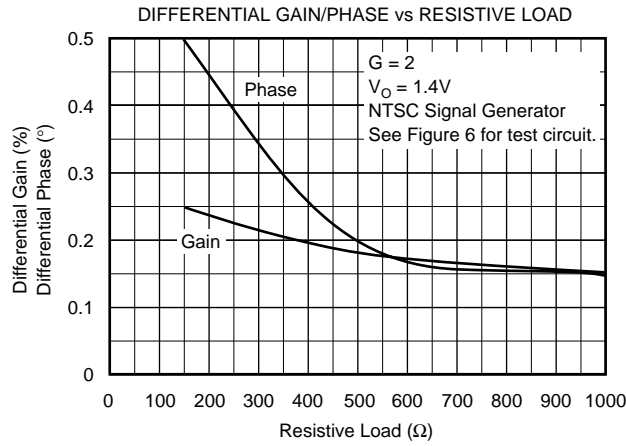
TYPICAL CHARACTERISTICS

At $T_A = +25^\circ\text{C}$, $V_S = +5\text{V}$, and $R_L = 1\text{k}\Omega$ connected to $V_S/2$, unless otherwise noted.



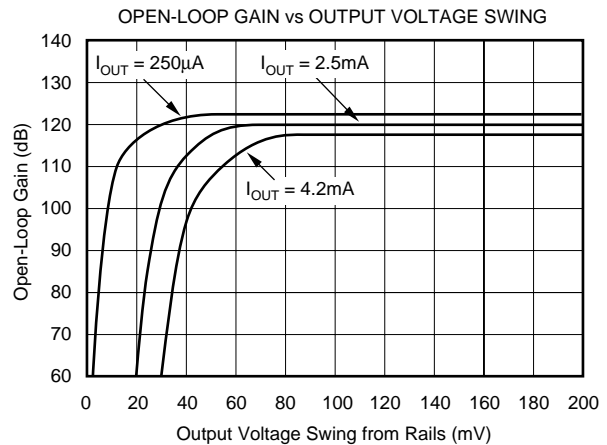
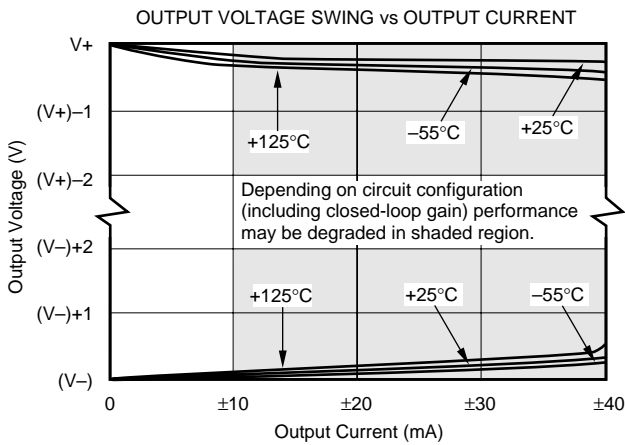
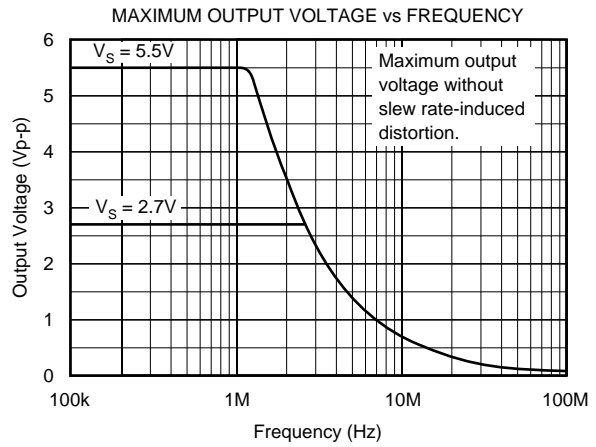
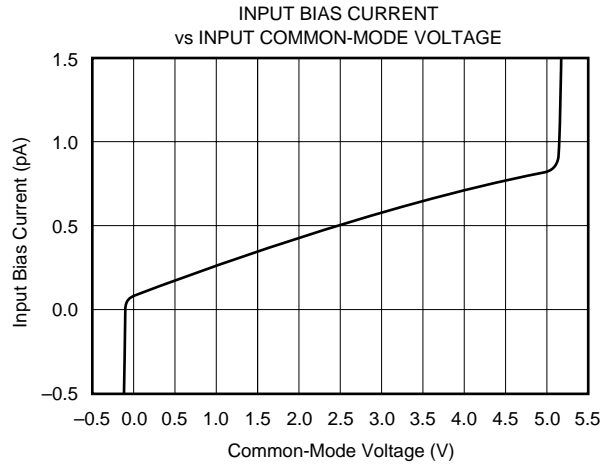
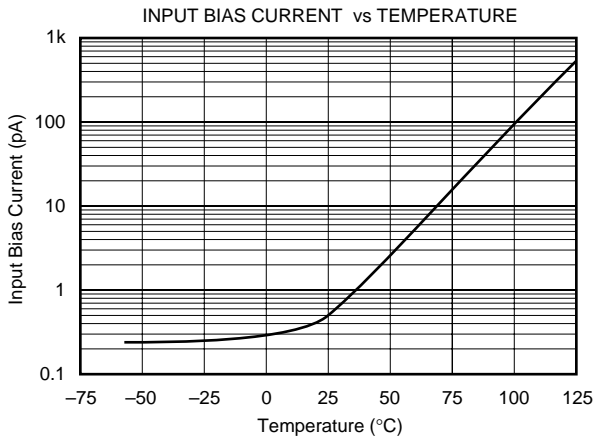
TYPICAL CHARACTERISTICS (Cont.)

At $T_A = +25^\circ\text{C}$, $V_S = +5\text{V}$, and $R_L = 1\text{k}\Omega$ connected to $V_S/2$, unless otherwise noted.



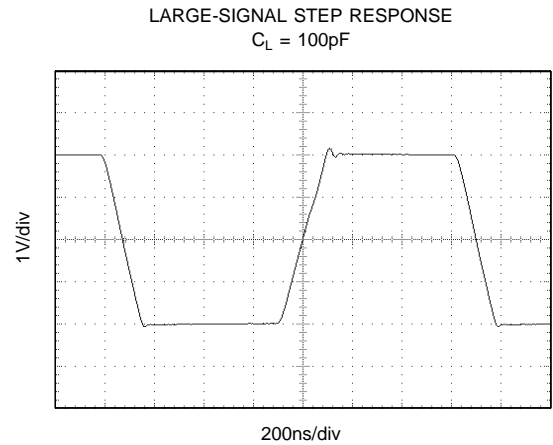
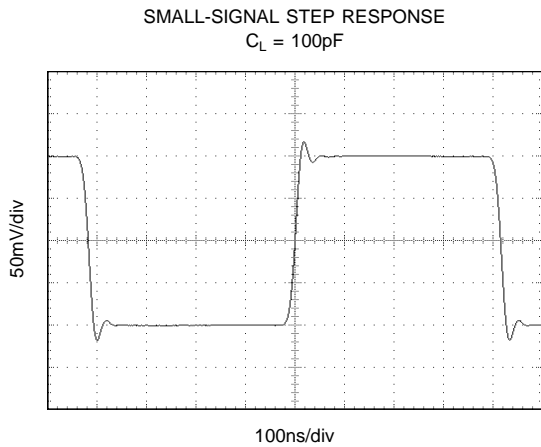
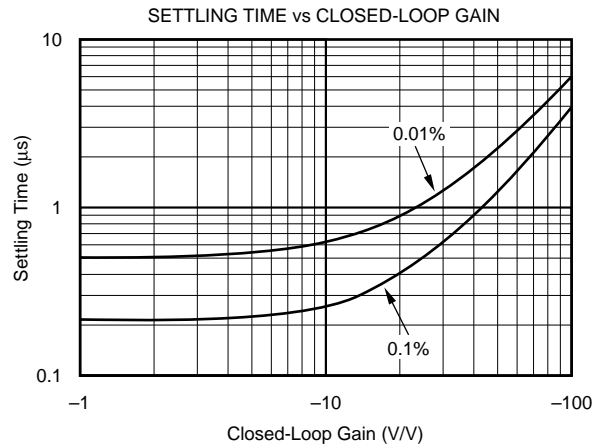
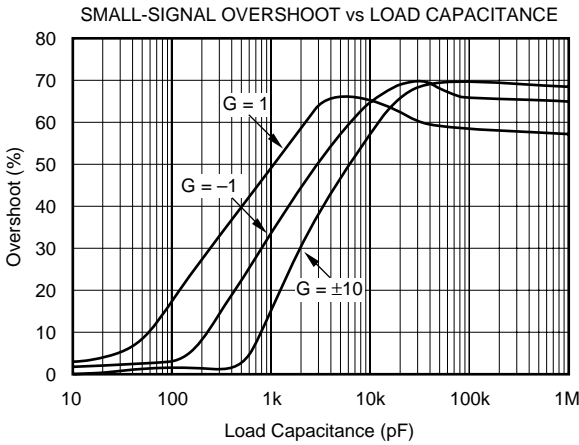
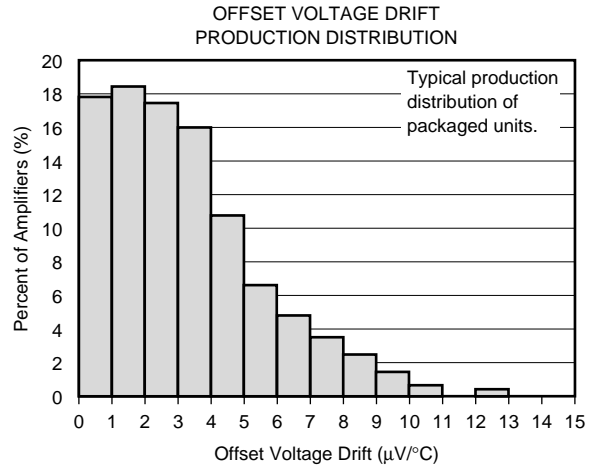
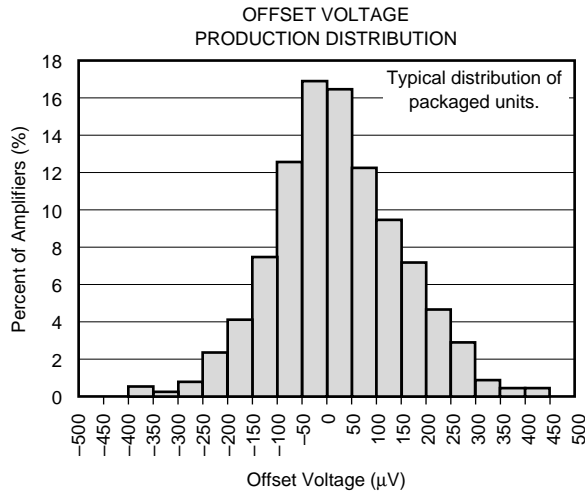
TYPICAL CHARACTERISTICS (Cont.)

At $T_A = +25^\circ\text{C}$, $V_S = +5\text{V}$, and $R_L = 1\text{k}\Omega$ connected to $V_S/2$, unless otherwise noted.



TYPICAL CHARACTERISTICS (Cont.)

At $T_A = +25^\circ\text{C}$, $V_S = +5\text{V}$, and $R_L = 1\text{k}\Omega$ connected to $V_S/2$, unless otherwise noted.



APPLICATIONS INFORMATION

OPA350 series op amps are fabricated on a state-of-the-art 0.6 micron CMOS process. They are unity-gain stable and suitable for a wide range of general-purpose applications. Rail-to-rail input/output make them ideal for driving sampling A/D converters. They are also well suited for controlling the output power in cell phones. These applications often require high speed and low noise. In addition, the OPA350 series offers a low cost solution for general-purpose and consumer video applications (75Ω drive capability).

Excellent ac performance makes the OPA350 series well suited for audio applications. Their bandwidth, slew rate, low noise (5nV/√Hz), low THD (0.0006%), and small package options are ideal for these applications. The class AB output stage is capable of driving 600Ω loads connected to any point between V+ and ground.

Rail-to-rail input and output swing significantly increases dynamic range, especially in low voltage supply applications. Figure 1 shows the input and output waveforms for

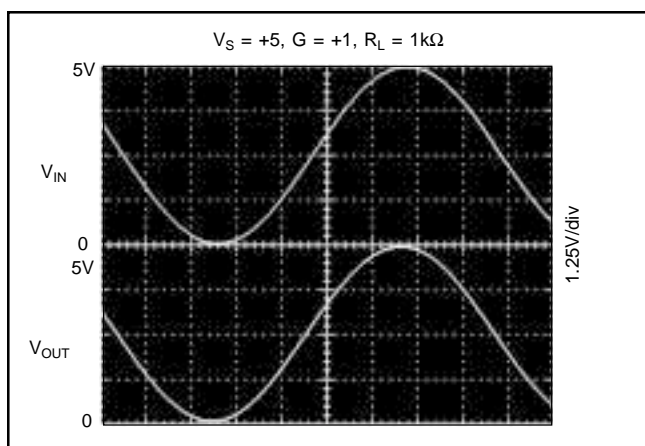


FIGURE 1. Rail-to-Rail Input and Output.

the OPA350 in unity-gain configuration. Operation is from a single +5V supply with a 1kΩ load connected to $V_S/2$. The input is a 5Vp-p sinusoid. Output voltage swing is approximately 4.95Vp-p.

Power supply pins should be bypassed with 0.01μF ceramic capacitors.

OPERATING VOLTAGE

OPA350 series op amps are fully specified from +2.7V to +5.5V. However, supply voltage may range from +2.5V to +5.5V. Parameters are tested over the specified supply range—a unique feature of the OPA350 series. In addition, many specifications apply from -40°C to +85°C. Most behavior remains virtually unchanged throughout the full operating voltage range. Parameters that vary significantly with operating voltage or temperature are shown in the typical characteristics.

RAIL-TO-RAIL INPUT

The tested input common-mode voltage range of the OPA350 series extends 100mV beyond the supply rails. This is achieved with a complementary input stage—an N-channel input differential pair in parallel with a P-channel differential pair, as shown on Figure 2. The N-channel pair is active for input voltages close to the positive rail, typically $(V_+) - 1.8V$ to 100mV above the positive supply, while the P-channel pair is on for inputs from 100mV below the negative supply to approximately $(V_+) - 1.8V$. There is a small transition region, typically $(V_+) - 2V$ to $(V_+) - 1.6V$, in which both pairs are on. This 400mV transition region can vary ±400mV with process variation. Thus, the transition region (both input stages on) can range from $(V_+) - 2.4V$ to $(V_+) - 2.0V$ on the low end, up to $(V_+) - 1.6V$ to $(V_+) - 1.2V$ on the high end.

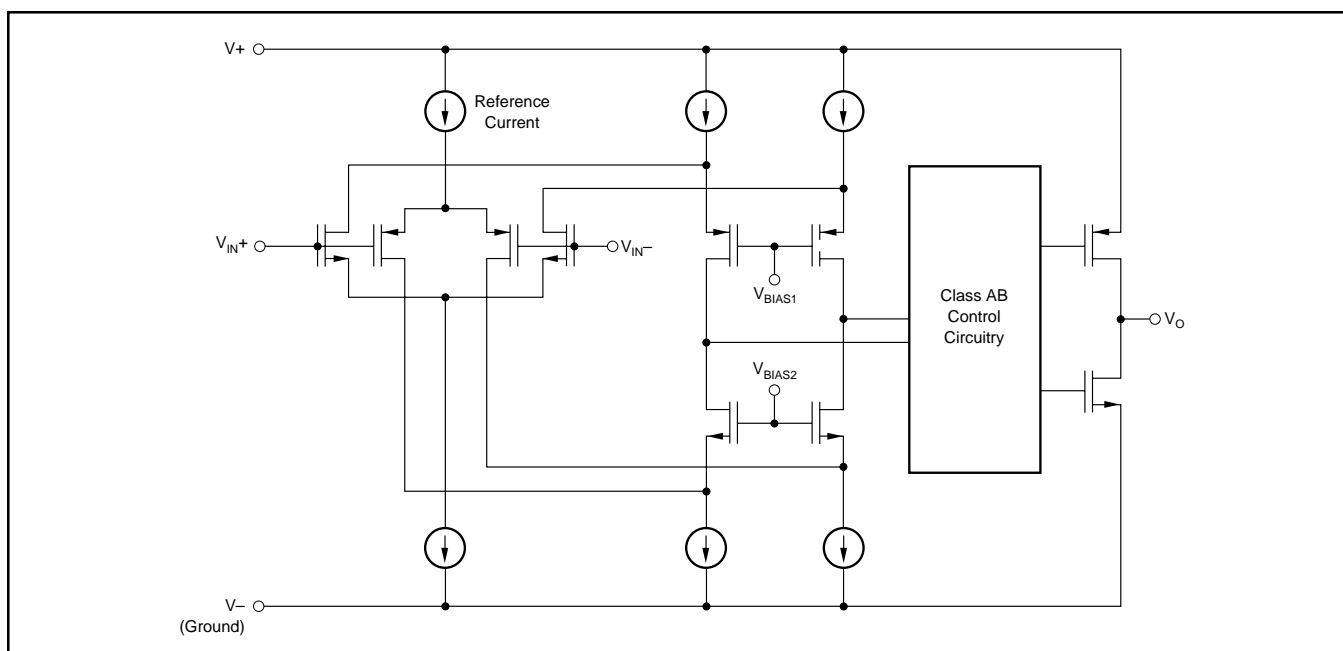


FIGURE 2. Simplified Schematic.

OPA350 series op amps are laser-trimmed to reduce offset voltage difference between the N-channel and P-channel input stages, resulting in improved common-mode rejection and a smooth transition between the N-channel pair and the P-channel pair. However, within the 400mV transition region PSRR, CMRR, offset voltage, offset drift, and THD may be degraded compared to operation outside this region.

A double-folded cascode adds the signal from the two input pairs and presents a differential signal to the class AB output stage. Normally, input bias current is approximately 500fA. However, large inputs (greater than 300mV beyond the supply rails) can turn on the OPA350's input protection diodes, causing excessive current to flow in or out of the input pins. Momentary voltages greater than 300mV beyond the power supply can be tolerated if the current on the input pins is limited to 10mA. This is easily accomplished with an input resistor, as shown in Figure 3. Many input signals are inherently current-limited to less than 10mA, therefore, a limiting resistor is not required.

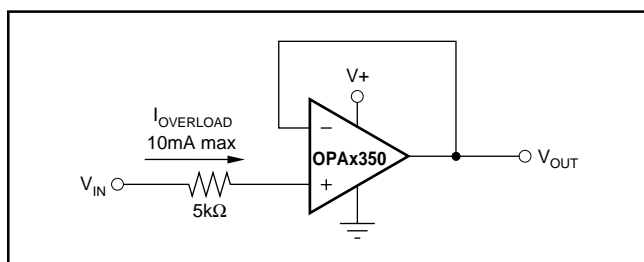


FIGURE 3. Input Current Protection for Voltages Exceeding the Supply Voltage.

RAIL-TO-RAIL OUTPUT

A class AB output stage with common-source transistors is used to achieve rail-to-rail output. For light resistive loads (>10kΩ), the output voltage swing is typically ten millivolts from the supply rails. With heavier resistive loads (600Ω to 10kΩ), the output can swing to within a few tens of millivolts from the supply rails and maintain high open-loop gain. See the typical characteristics “Output Voltage Swing vs Output Current” and “Open-Loop Gain vs Output Voltage.”

CAPACITIVE LOAD AND STABILITY

OPA350 series op amps can drive a wide range of capacitive loads. However, all op amps under certain conditions may become unstable. Op amp configuration, gain, and load value are just a few of the factors to consider when determining stability. An op amp in unity-gain configuration is the most susceptible to the effects of capacitive load. The capacitive load reacts with the op amp's output impedance, along with any additional load resistance, to create a pole in the small-signal response that degrades the phase margin.

In unity gain, OPA350 series op amps perform well with very large capacitive loads. Increasing gain enhances the amplifier's ability to drive more capacitance. The typical

characteristic “Small-Signal Overshoot vs Capacitive Load” shows performance with a 1kΩ resistive load. Increasing load resistance improves capacitive load drive capability.

FEEDBACK CAPACITOR IMPROVES RESPONSE

For optimum settling time and stability with high-impedance feedback networks, it may be necessary to add a feedback capacitor across the feedback resistor, R_F , as shown in Figure 4. This capacitor compensates for the zero created by the feedback network impedance and the OPA350's input capacitance (and any parasitic layout capacitance). The effect becomes more significant with higher impedance networks.

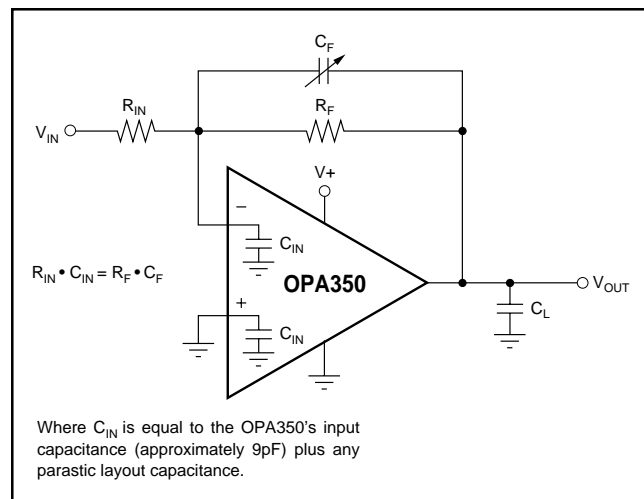


FIGURE 4. Feedback Capacitor Improves Dynamic Performance.

It is suggested that a variable capacitor be used for the feedback capacitor since input capacitance may vary between op amps and layout capacitance is difficult to determine. For the circuit shown in Figure 4, the value of the variable feedback capacitor should be chosen so that the input resistance times the input capacitance of the OPA350 (typically 9pF) plus the estimated parasitic layout capacitance equals the feedback capacitor times the feedback resistor:

$$R_{IN} \cdot C_{IN} = R_F \cdot C_F$$

where C_{IN} is equal to the OPA350's input capacitance (sum of differential and common-mode) plus the layout capacitance. The capacitor can be varied until optimum performance is obtained.

DRIVING A/D CONVERTERS

OPA350 series op amps are optimized for driving medium speed (up to 500kHz) sampling A/D converters. However, they also offer excellent performance for higher speed converters. The OPA350 series provides an effective means of buffering the A/D's input capacitance and resulting charge injection while providing signal gain.

Figure 5 shows the OPA350 driving an ADS7861. The ADS7861 is a dual, 500kHz, 12-bit sampling converter in the tiny SSOP-24 package. When used with the miniature package options of the OPA350 series, the combination is ideal for space-limited applications. For further information, consult the ADS7861 data sheet (SBAS110A).

OUTPUT IMPEDANCE

The low frequency open-loop output impedance of the OPA350's common-source output stage is approximately $1k\Omega$. When the op amp is connected with feedback, this value is reduced significantly by the loop gain of the op amp. For example, with 122dB of open-loop gain, the output impedance is reduced in unity-gain to less than 0.001Ω . For each decade rise in the closed-loop gain, the loop gain is reduced by the same amount which results in a ten-fold increase in effective output impedance (see the typical characteristic, "Output Impedance vs Frequency").

At higher frequencies, the output impedance will rise as the open-loop gain of the op amp drops. However, at these frequencies the output also becomes capacitive due to parasitic capacitance. This prevents the output impedance from becoming too high, which can cause stability prob-

lems when driving capacitive loads. As mentioned previously, the OPA350 has excellent capacitive load drive capability for an op amp with its bandwidth.

VIDEO LINE DRIVER

Figure 6 shows a circuit for a single supply, $G = 2$ composite video line driver. The synchronized outputs of a composite video line driver extend below ground. As shown, the input to the op amp should be ac-coupled and shifted positively to provide adequate signal swing to account for these negative signals in a single-supply configuration.

The input is terminated with a 75Ω resistor and ac-coupled with a $47\mu F$ capacitor to a voltage divider that provides the dc bias point to the input. In Figure 6, this point is approximately $(V-) + 1.7V$. Setting the optimal bias point requires some understanding of the nature of composite video signals. For best performance, one should be careful to avoid the distortion caused by the transition region of the OPA350's complementary input stage. Refer to the discussion of rail-to-rail input.

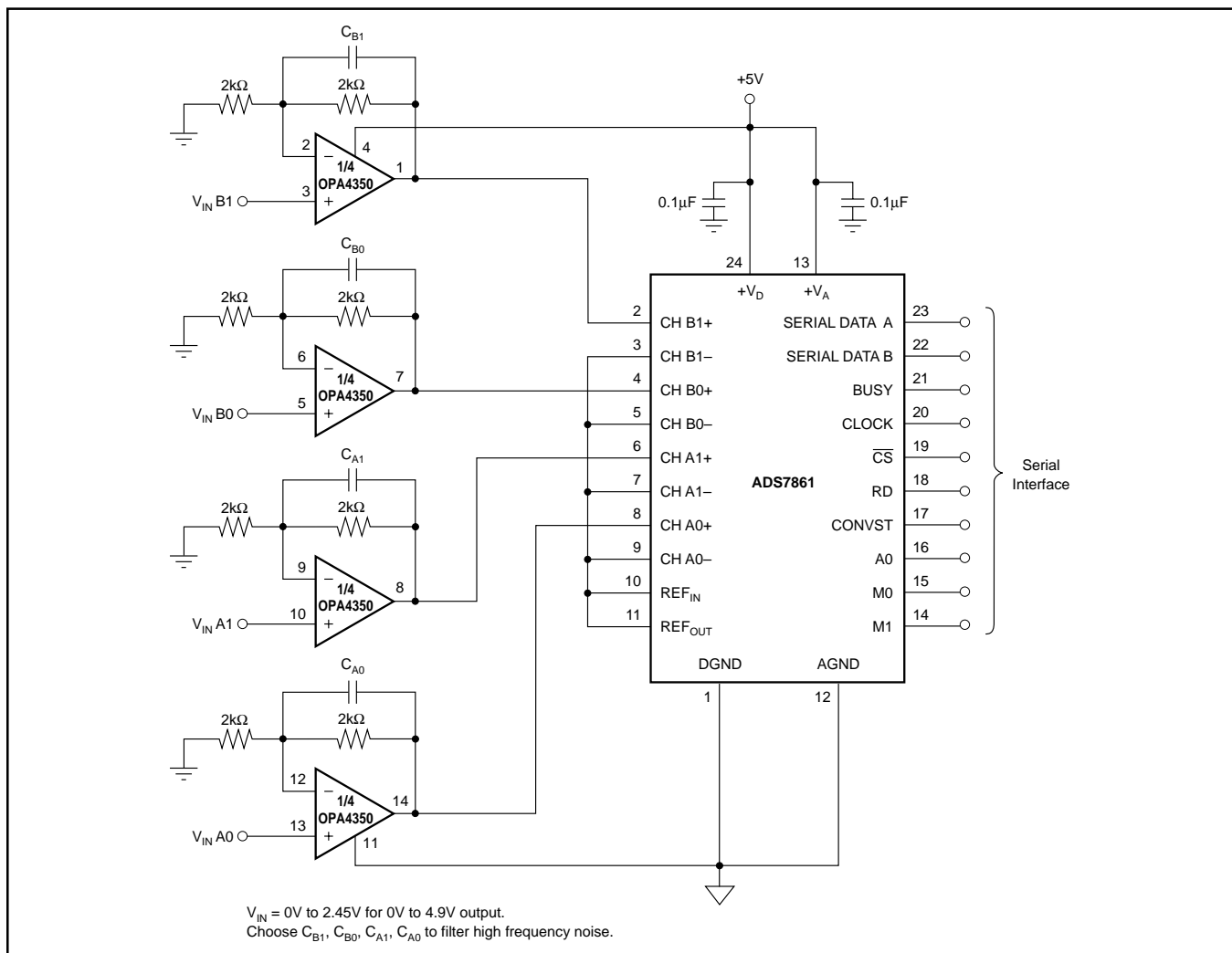


FIGURE 5. OPA4350 Driving Sampling A/D Converter.

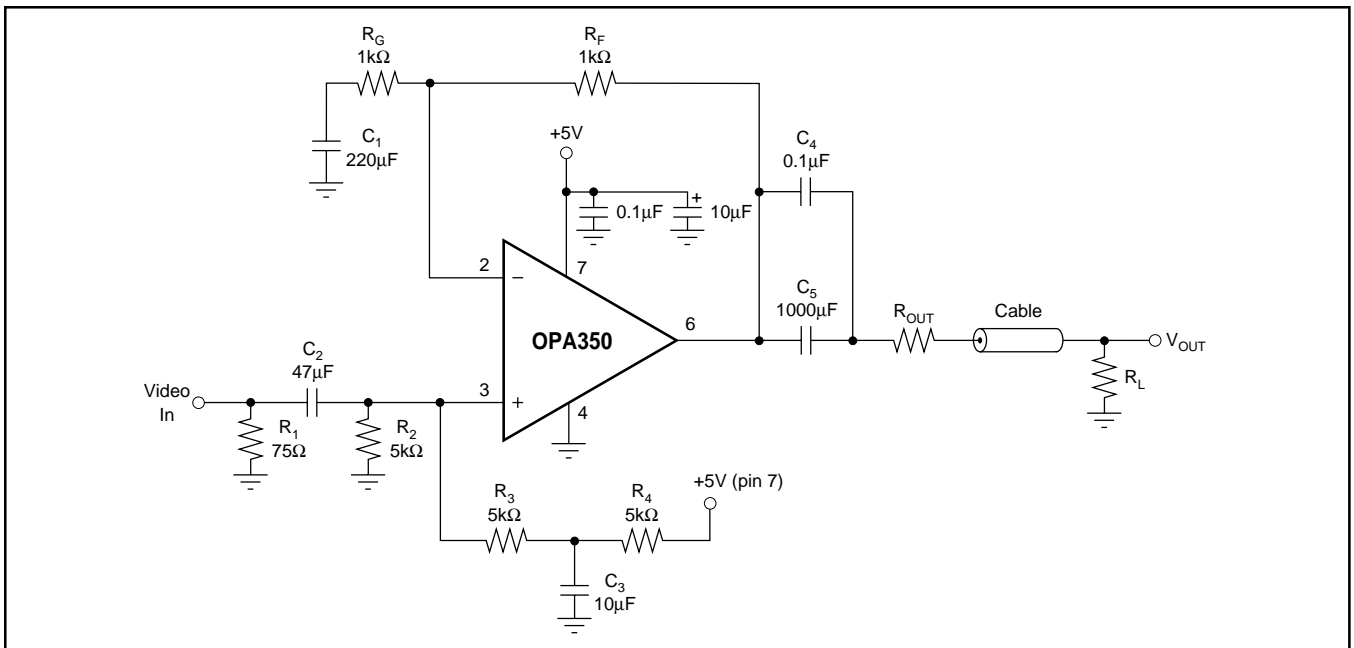


FIGURE 6. Single-Supply Video Line Driver.

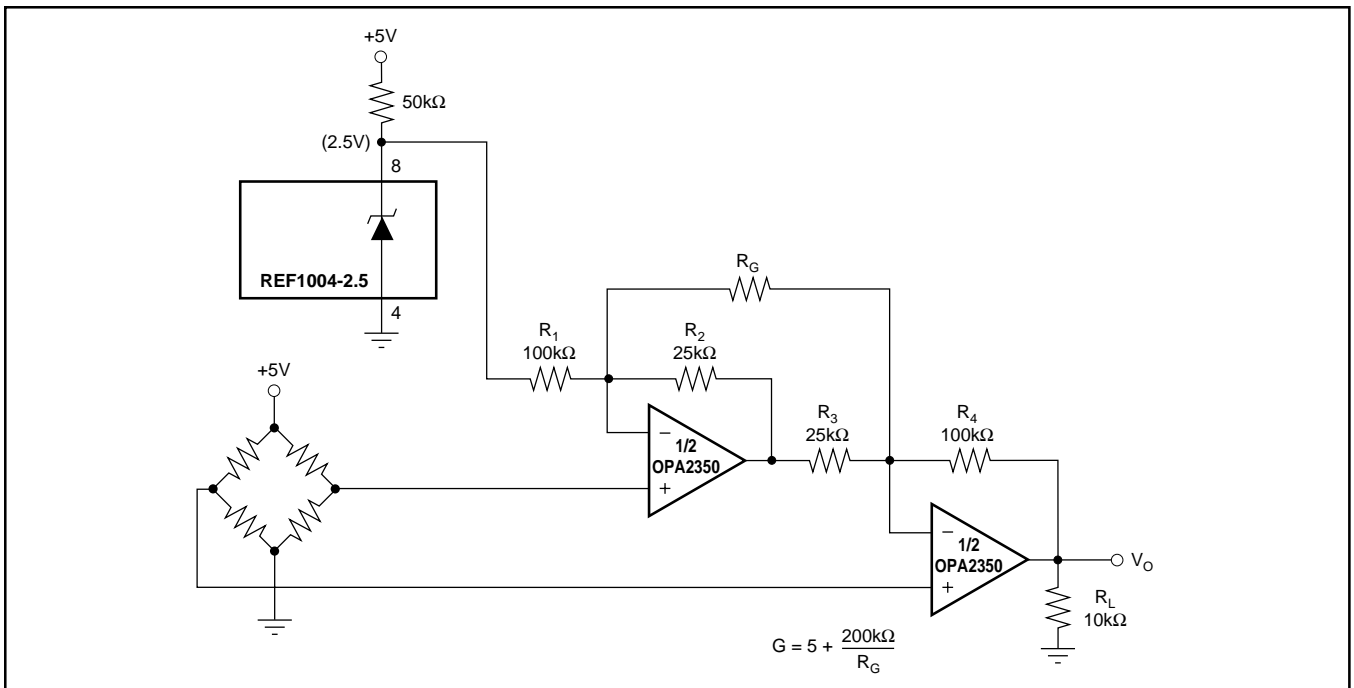


FIGURE 7. Two Op-Amp Instrumentation Amplifier With Improved High Frequency Common-Mode Rejection.

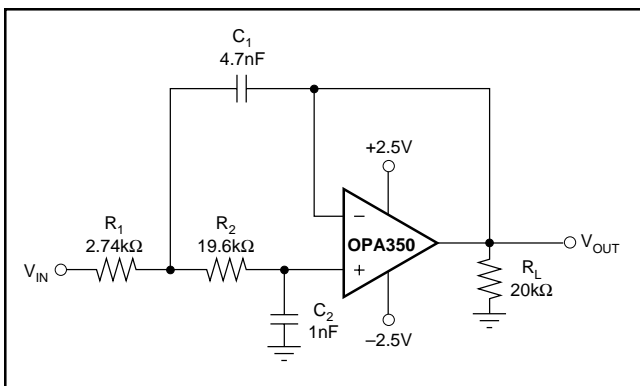


FIGURE 8. 10kHz Low-Pass Filter.

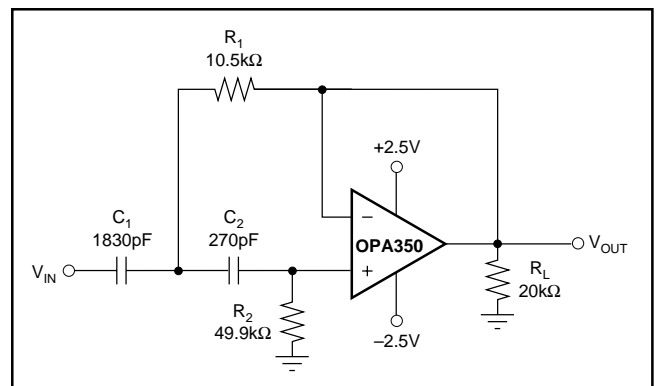


FIGURE 9. 10kHz High-Pass Filter.

IMPORTANT NOTICE

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgment, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

Customers are responsible for their applications using TI components.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, license, warranty or endorsement thereof.

Reproduction of information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations and notices. Representation or reproduction of this information with alteration voids all warranties provided for an associated TI product or service, is an unfair and deceptive business practice, and TI is not responsible nor liable for any such use.

Resale of TI's products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service, is an unfair and deceptive business practice, and TI is not responsible nor liable for any such use.

Also see: [Standard Terms and Conditions of Sale for Semiconductor Products](http://www.ti.com/sc/docs/stdterms.htm). www.ti.com/sc/docs/stdterms.htm

Mailing Address:

Texas Instruments
Post Office Box 655303
Dallas, Texas 75265