

NJM4556A

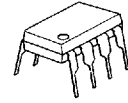
The NJM4556A integrated circuit is a high-gain, high output current dual operational amplifier capable of driving $\pm 70\text{mA}$ into 15Ω loads ($\pm 10.5\text{V}$ output voltage), and operating Low supply voltage ($V^+/V^- = \pm 2\text{V} \sim$).

The NJM4556A combines many of the features of the popular NJM4558 as well as having the capability of driving 150Ω loads. In addition, the wide band-width, low noise, high slew rate and low distortion of the NJM4556A make it ideal for many audio, telecommunications and instrumentation applications.

■ Absolute Maximum Ratings (Ta=25°C)

Supply Voltage	V^+/V^-	$\pm 18\text{V}$
Differential Input Voltage	V_{ID}	$\pm 30\text{V}$
Input Voltage(note)	V_i	$\pm 15\text{V}$
Power Dissipation	P_D (D-Type)	700mW
	(M-Type)	300mW
	(V-Type)	250mW
	(L-Type)	800mW
Operating Temperature Range	T_{opr}	$-20 \sim +75^\circ\text{C}$
Storage Temperature Range	T_{stg}	$-40 \sim +125^\circ\text{C}$

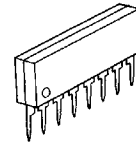
■ Package Outline



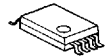
NJM4556AD



NJM4556AM



NJM4556AL



NJM4556AV

(note) For supply voltage less than $\pm 15\text{V}$, the absolute maximum input voltage is equal to the supply voltage.

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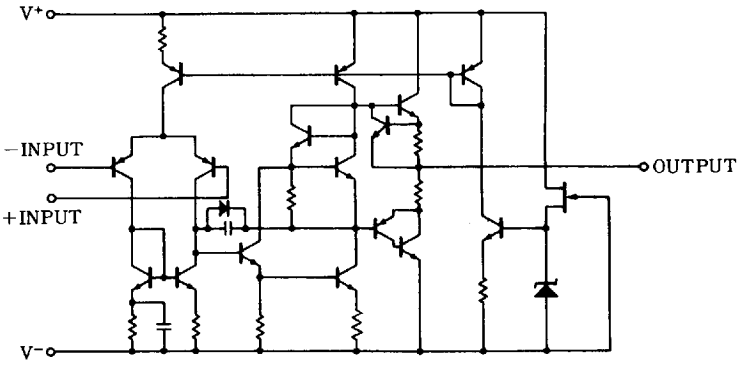
■ Electrical Characteristics (NJM4556D/NJM4556S) ($V^+/V^- = \pm 15\text{V}$, Ta=25°C)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Input Offset Voltage	V_{IO}	$R_s \leq 10\text{k}\Omega$	—	0.5	6.0	mV
Input Offset Current	I_{IO}		—	5	60	nA
Input Bias Current	I_B		—	50	500	nA
Input Resistance	R_{IN}		0.3	5	—	M Ω
Large Signal Voltage Gain	A_V	$R_L \geq 2\text{k}\Omega$, $V_O = \pm 10\text{V}$	86	100	—	dB
Maximum Output Voltage Swing 1	V_{OM1}	$R_L \geq 2\text{k}\Omega$	± 12	± 13.5	—	V
Maximum Output Voltage Swing 2	V_{OM2}	$R_L \geq 150\Omega$	± 10.5	± 11	—	V
Input Common Mode Voltage Range	V_{ICM}		± 13.5	± 14	—	V
Common Mode Rejection Ratio	CMR	$R_s \leq 10\text{k}\Omega$	70	90	—	dB
Supply Voltage Rejection Ratio	SVR	$R_s \leq 10\text{k}\Omega$	76.5	90	—	dB
Supply Current	I_{CC}		—	9	12	mA
Slew Rate	SR		—	3	—	V/ μs
Unity Gain Bandwidth	GB		—	8	—	MHz

■ Electrical Characteristics (NJM4556AM/NJM4556AV) ($V^+/V^- = \pm 15\text{V}$, Ta=25°C)

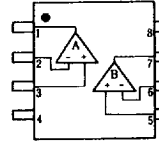
Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Input Offset Voltage	V_{IO}	$R_s \leq 10\text{k}\Omega$	—	0.5	6.0	mV
Input Offset Current	I_{IO}		—	5	60	nA
Input Bias Current	I_B		—	50	500	nA
Large Signal Voltage Gain	A_V	$R_L \geq 2\text{k}\Omega$, $V_O = \pm 10\text{V}$	86	100	—	dB
Maximum Output Voltage Swing 1	V_{OM1}	$V_{IN+}=4\text{V}$, $V_{IN-}=3\text{V}$, $V^+=9\text{V}$ $I_{source}=40\text{mA}$	7.5	—	—	V
Maximum Output Voltage Swing 2	V_{OM2}	$V_{IN+}=3\text{V}$, $V_{IN-}=4\text{V}$, $V^+=9\text{V}$ $I_{sink}=40\text{mA}$	—	—	2.1	V
Input Common Mode Voltage Range 1	V_{ICM1}	$V^+=9\text{V}$, V_{IL}	—	—	1.5	V
Input Common Mode Voltage Range 2	V_{ICM2}	$V^+=9\text{V}$, V_{IH}	8	—	—	V
Common Mode Rejection Ratio	CMR	$R_s \leq 10\text{k}\Omega$	70	90	—	dB
Supply Voltage Rejection Ratio	SVR	$R_s \leq 10\text{k}\Omega$	76.5	90	—	dB
Supply Current	I_{CC}	$V^+=9\text{V}$	—	8	12	mA
Slew Rate	SR		—	3	—	V/ μs
Unity Gain Bandwidth	GB		—	8	—	MHz

■ Equivalent Circuit (1/2 Shown)



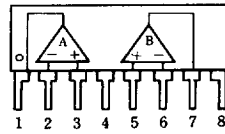
■ Connection Diagram

D,M,V-Type
(Top View)



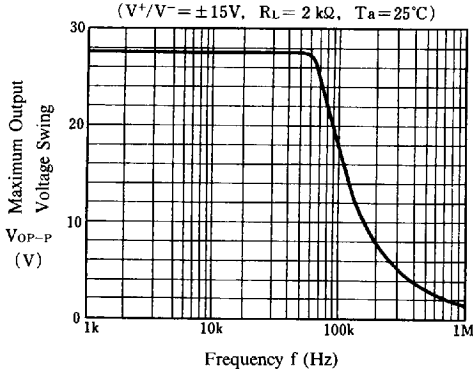
- PIN FUNCTION
- 1. A OUTPUT
 - 2. A -INPUT
 - 3. A +INPUT
 - 4. V-
 - 5. B +INPUT
 - 6. B -INPUT
 - 7. B OUTPUT
 - 8. V+

(L-Type)

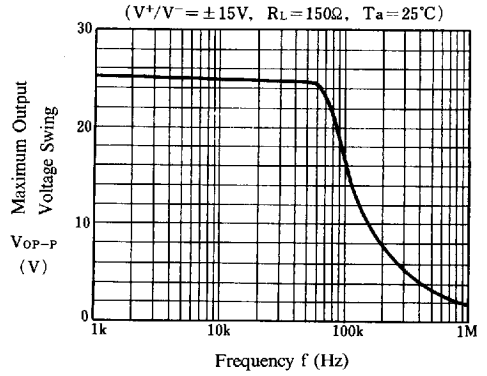


■ Typical Characteristics

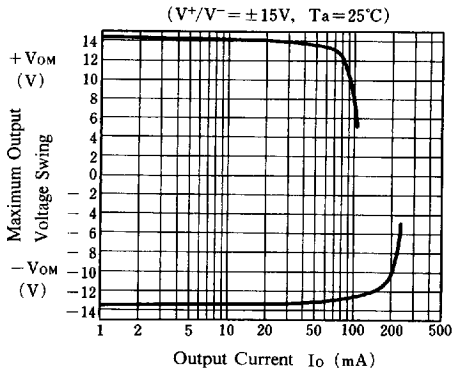
Maximum Output Voltage Swing vs. Frequency



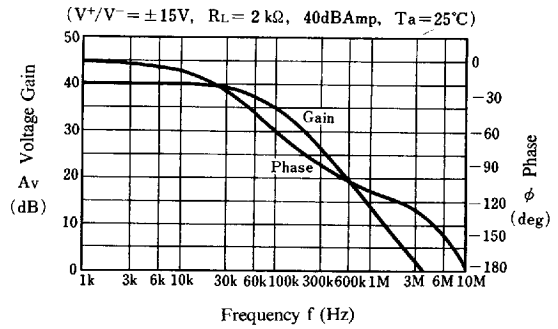
Maximum Output Voltage Swing vs. Frequency



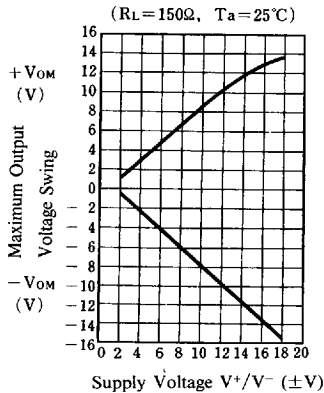
Maximum Output Voltage Swing vs. Output Current



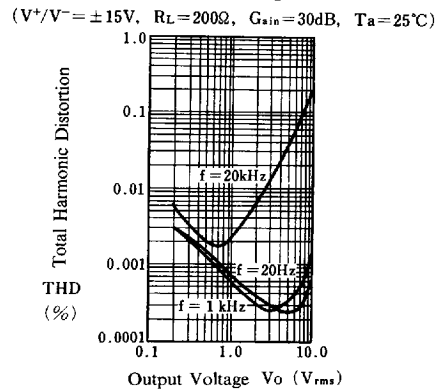
Voltage Gain, Phase Shift vs. Frequency



Maximum Output Voltage Swing vs. Supply Voltage



Total Harmonic Distortion vs. Output Voltage



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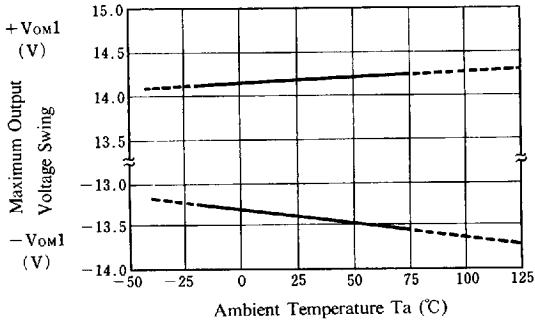
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■ Typical Characteristics

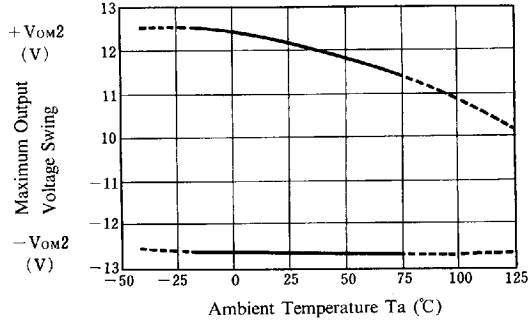
Maximum Output Voltage Swing vs. Temperature

($V^+/V^- = \pm 15V$, $R_L = 2k\Omega$)



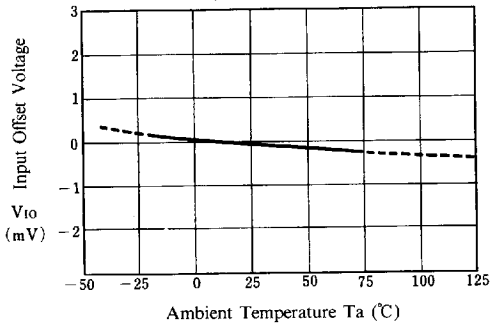
Maximum Output Voltage Swing vs. Temperature

($V^+/V^- = \pm 15V$, $R_L = 150\Omega$)



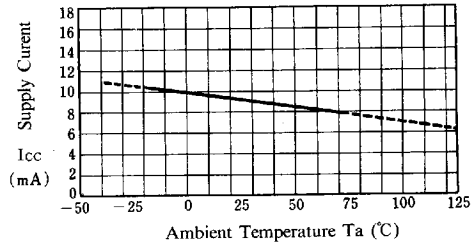
Input Offset Voltage vs. Temperature

($V^+/V^- = \pm 15V$)



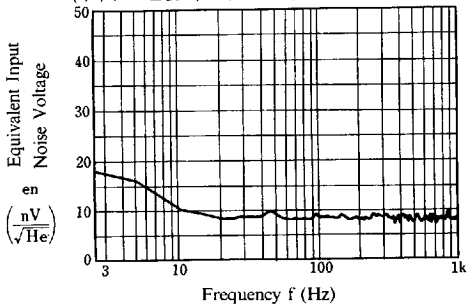
Supply Current vs. Temperature

($V^+/V^- = \pm 15V$)



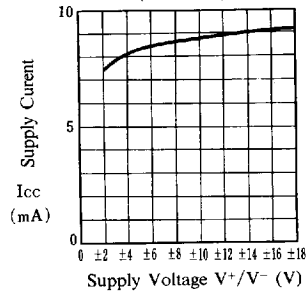
Equivalent Input Noise Voltage vs. Frequency

($V^-/V^+ = \pm 15V$, $R_s = 100\Omega$, $A_v = 40dB$, $T_a = 25^\circ C$)



Supply Current vs. Supply Voltage

($T_a = 25^\circ C$)



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