

# Three quadrant triacs high commutation

# BTA208S series B BTA208M series B

## GENERAL DESCRIPTION

Glass passivated high commutation triacs in a plastic envelope, suitable for surface mounting, intended for use in circuits where high static and dynamic dV/dt and high dI/dt can occur. These devices will commute the full rated rms current at the maximum rated junction temperature, without the aid of a snubber.

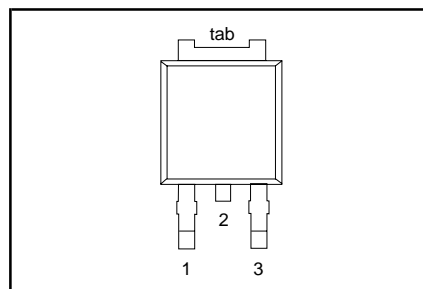
## QUICK REFERENCE DATA

| SYMBOL       | PARAMETER  | MAX.               | MAX.               | MAX.               | UNIT |
|--------------|--|--------------------|--------------------|--------------------|------|
| $V_{DRM}$    | <b>BTA208S</b> (or BTA208M)-<br>Repetitive peak off-state voltages | <b>500B</b><br>500 | <b>600B</b><br>600 | <b>800B</b><br>800 | V    |
| $I_{T(RMS)}$ | RMS on-state current   | 8                  | 8                  | 8                  | A    |
| $I_{TSM}$    | Non-repetitive peak on-state current                               | 65                 | 65                 | 65                 | A    |

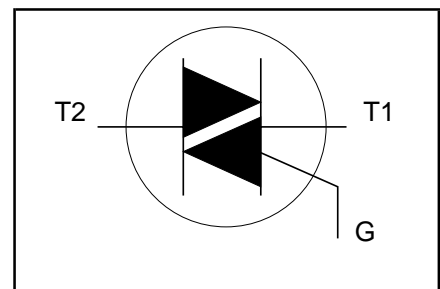
## PINNING - SOT428

| PIN NUMBER | Standard S | Alternative M |
|------------|------------|---------------|
| 1          | MT1        | gate          |
| 2          | MT2        | MT2           |
| 3          | gate       | MT1           |
| tab        | MT2        | MT2           |

## PIN CONFIGURATION



## SYMBOL



## LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

| SYMBOL       | PARAMETER  | CONDITIONS   | MIN. | MAX.                     |                          |             | UNIT             |
|--------------|--|--|------|--------------------------|--------------------------|-------------|------------------|
|              |  |  |      | -500<br>500 <sup>1</sup> | -600<br>600 <sup>1</sup> | -800<br>800 |                  |
| $V_{DRM}$    | Repetitive peak off-state voltages                           |  | -    |                          |                          |             | V                |
| $I_{T(RMS)}$ | RMS on-state current   | full sine wave;<br>$T_{mb} \leq 102^\circ\text{C}$   | -    | 8                        |                          |             | A                |
| $I_{TSM}$    | Non-repetitive peak on-state current                         | full sine wave;<br>$T_j = 25^\circ\text{C}$ prior to surge   | -    | 65                       |                          |             | A                |
| $I^2t$       | $I^2t$ for fusing  | $t = 20\text{ ms}$   | -    | 71                       |                          |             | A                |
| $di_T/dt$    | Repetitive rate of rise of on-state current after triggering | $t = 10\text{ ms}$<br>$I_{TM} = 12\text{ A}; I_G = 0.2\text{ A}; di_G/dt = 0.2\text{ A}/\mu\text{s}$ | -    | 21                       |                          |             | A <sup>2</sup> s |
| $I_{GM}$     | Peak gate current  |  | -    | 2                        |                          |             | A                |
| $V_{GM}$     | Peak gate voltage  |  | -    | 5                        |                          |             | V                |
| $P_{GM}$     | Peak gate power  |  | -    | 5                        |                          |             | W                |
| $P_{G(AV)}$  | Average gate power   | over any 20 ms period  | -    | 0.5                      |                          |             | W                |
| $T_{stg}$    | Storage temperature  |  | -40  | 150                      |                          |             | $^\circ\text{C}$ |
| $T_j$        | Operating junction temperature                               |  | -    | 125                      |                          |             | $^\circ\text{C}$ |

<sup>1</sup> Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 6 A/ $\mu\text{s}$ .

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### THERMAL RESISTANCES

| SYMBOL         | PARAMETER                                       | CONDITIONS                                | MIN. | TYP. | MAX. | UNIT |
|----------------|---|---|------|------|------|------|
| $R_{th\ j-mb}$ | Thermal resistance<br>junction to mounting base | full cycle                                | -    | -    | 2.0  | K/W  |
|                |   | half cycle                                | -    | -    | 2.4  | K/W  |
| $R_{th\ j-a}$  | Thermal resistance<br>junction to ambient       | pcb (FR4) mounted; footprint as in Fig.14 | -    | 75   | -    | K/W  |

### STATIC CHARACTERISTICS

$T_j = 25\text{ °C}$  unless otherwise stated

| SYMBOL   | PARAMETER                         | CONDITIONS  | MIN. | TYP. | MAX. | UNIT |
|----------|-----------------------------------|---|------|------|------|------|
| $I_{GT}$ | Gate trigger current <sup>2</sup> | $V_D = 12\text{ V}; I_T = 0.1\text{ A}$                       |      |      |      |      |
|          |                                   | T2+ G+  | 2    | 18   | 50   | mA   |
|          |                                   | T2+ G-  | 2    | 21   | 50   | mA   |
|          |                                   | T2- G-  | 2    | 34   | 50   | mA   |
| $I_L$    | Latching current                  | $V_D = 12\text{ V}; I_{GT} = 0.1\text{ A}$                    |      |      |      |      |
|          |                                   | T2+ G+  | -    | 31   | 60   | mA   |
|          |                                   | T2+ G-  | -    | 34   | 90   | mA   |
|          |                                   | T2- G-  | -    | 30   | 60   | mA   |
| $I_H$    | Holding current                   | $V_D = 12\text{ V}; I_{GT} = 0.1\text{ A}$                    | -    | 31   | 60   | mA   |
| $V_T$    | On-state voltage                  | $I_T = 10\text{ A}$   | -    | 1.3  | 1.65 | V    |
| $V_{GT}$ | Gate trigger voltage              | $V_D = 12\text{ V}; I_T = 0.1\text{ A}$                       | -    | 0.7  | 1.5  | V    |
|          |                                   | $V_D = 400\text{ V}; I_T = 0.1\text{ A}; T_j = 125\text{ °C}$ | 0.25 | 0.4  | -    | V    |
| $I_D$    | Off-state leakage current         | $V_D = V_{DRM(max)}; T_j = 125\text{ °C}$                     | -    | 0.1  | 0.5  | mA   |

### DYNAMIC CHARACTERISTICS

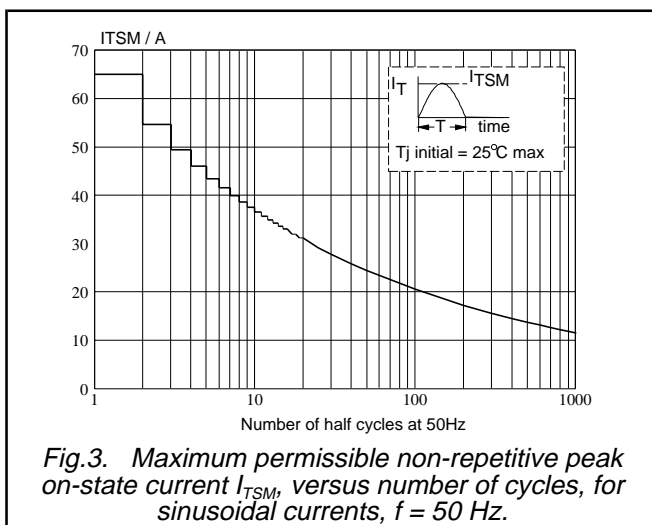
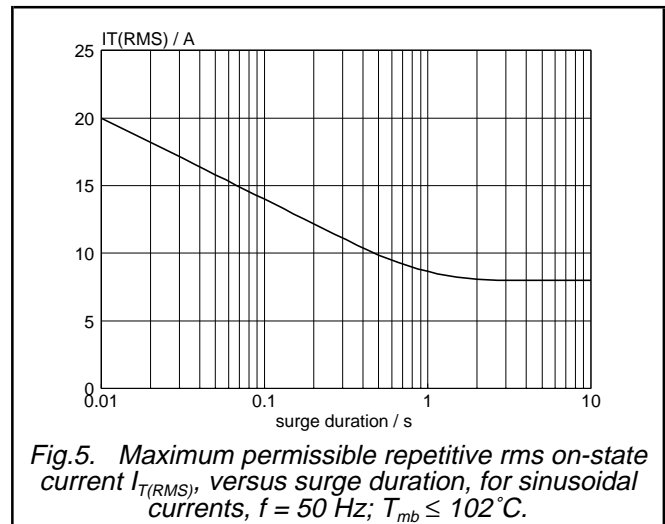
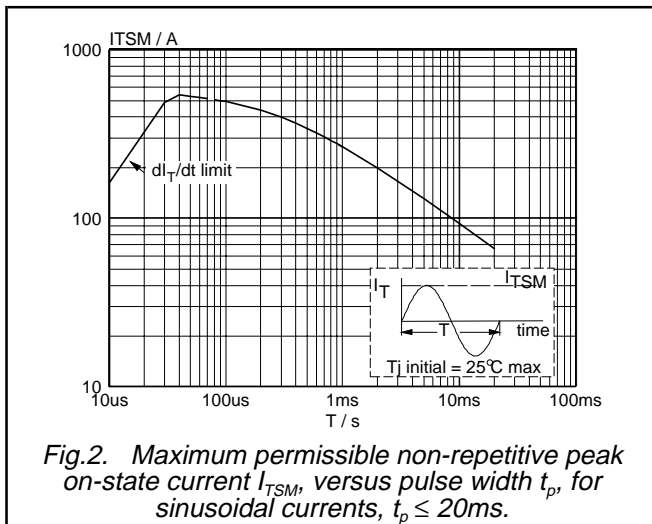
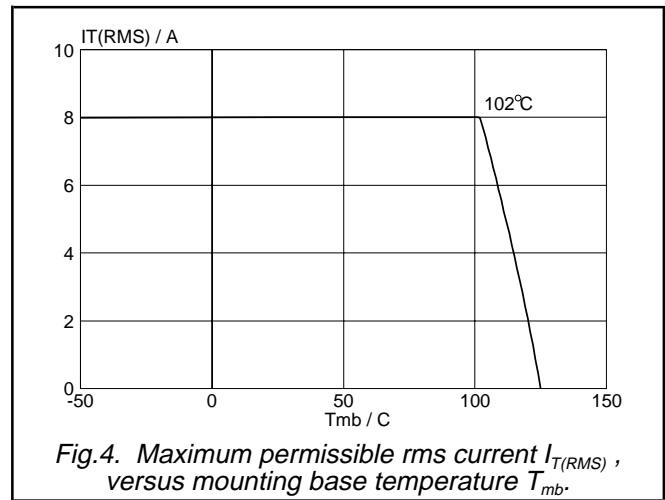
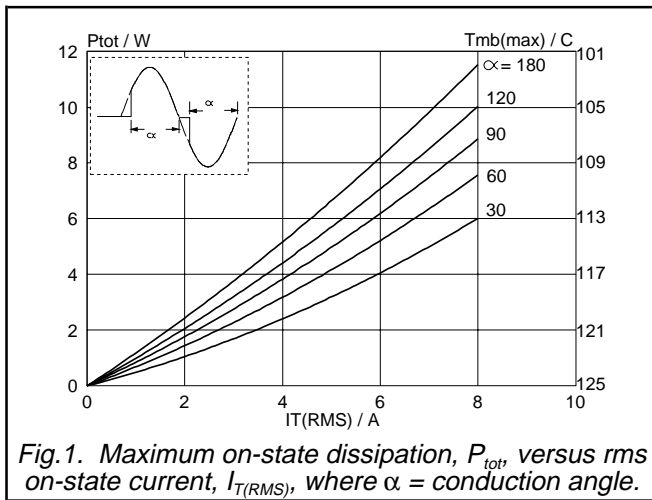
$T_j = 25\text{ °C}$  unless otherwise stated

| SYMBOL        | PARAMETER   | CONDITIONS   | MIN. | TYP. | MAX. | UNIT       |
|---------------|---|--|------|------|------|------------|
| $dV_D/dt$     | Critical rate of rise of<br>off-state voltage     | $V_{DM} = 67\% V_{DRM(max)}; T_j = 125\text{ °C};$<br>exponential waveform; gate open circuit                | 1000 | 4000 | -    | V/ $\mu$ s |
| $dI_{com}/dt$ | Critical rate of change of<br>commutating current | $V_{DM} = 400\text{ V}; T_j = 125\text{ °C}; I_{T(RMS)} = 8\text{ A};$<br>without snubber; gate open circuit | -    | 14   | -    | A/ms       |
| $t_{gt}$      | Gate controlled turn-on<br>time                   | $I_{TM} = 12\text{ A}; V_D = V_{DRM(max)}; I_G = 0.1\text{ A};$<br>$dI_G/dt = 5\text{ A}/\mu\text{s}$        | -    | 2    | -    | $\mu$ s    |

<sup>2</sup> Device does not trigger in the T2-, G+ quadrant.

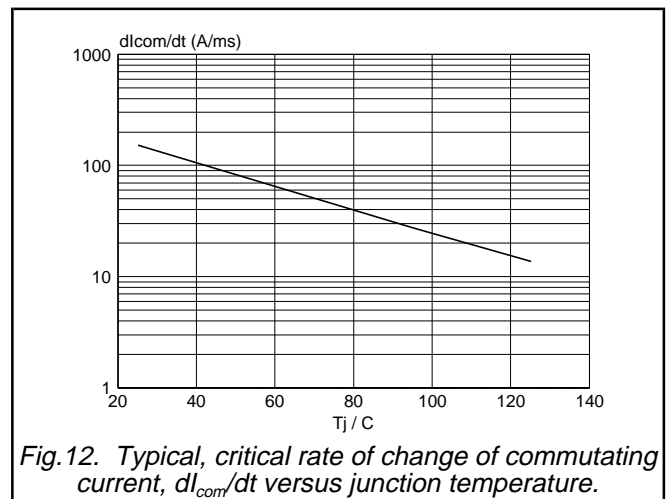
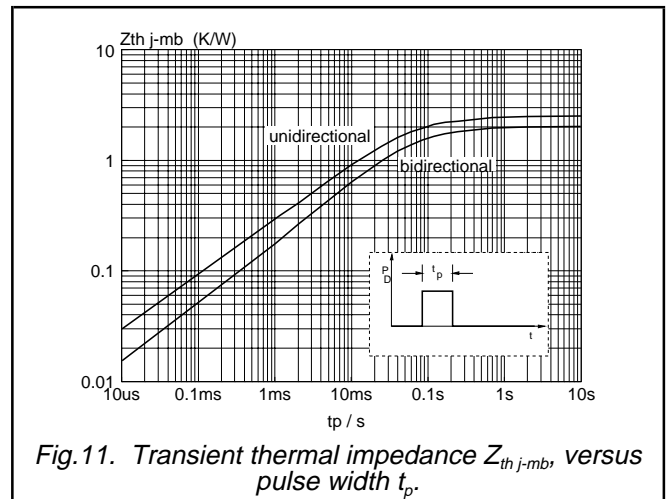
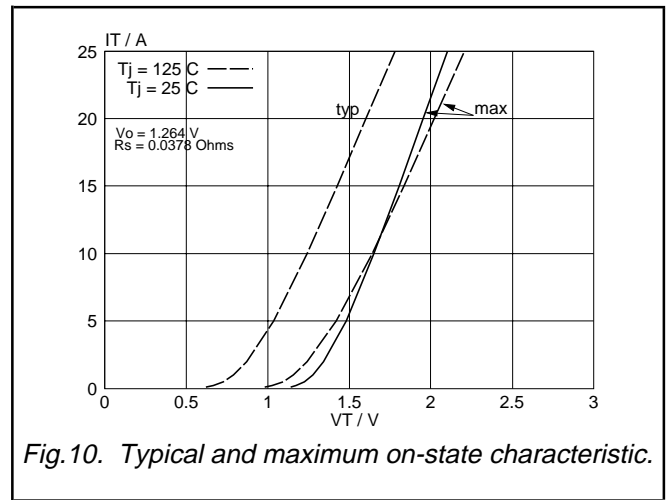
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### DEFINITIONS

|  |   |
|--|---|
| <b>Data sheet status</b>   |   |
| Objective specification  | This data sheet contains target or goal specifications for product development.       |
| Preliminary specification  | This data sheet contains preliminary data; supplementary data may be published later. |
| Product specification  | This data sheet contains final product specifications.                                |
| <b>Limiting values</b>   |   |
| Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability. |   |
| <b>Application information</b>   |   |
| Where application information is given, it is advisory and does not form part of the specification.  |   |
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